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from Osaka University, Osaka, Japan, in ...
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(WO/2005/111831) **PHYSICS** PROCESSING UNIT INSTRUCTION SET ARCHITECTURE
... **floating**¬ point operations on **vector** data contained in the **physics data**. .... The PPU of
claim 34, wherein the **VLIW** comprises a first slot containing ...
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Vector processor - Wikipedia, the free encyclopedia
Oregon-based **Floating Point** Systems (FPS) built add-on array processors for .... centres
and **physics** labs, where huge amounts of **data** are "crunched". ...
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The PhysX PPU (**Physics** Processor Unit) chip is made up of 3 engines along with ... For
example, **data** could be written to one of the **Vector** Processing Elements ... The **Floating**
Point Engine is the part of the PPU which does the real work, ... instruction and a 6 part
vector instruction as a single **VLIW** (Very Long ...
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[PDF] **FR500 VLIW**-architecture High-performance Embedded Microprocessor
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by VT Sukemura - 2000 - Cited by 8 - Related articles
sibility of bottlenecks in the **data** supply to the **floating**-point or media processing units.
Also, a .... optimization technology of the **VLIW** and **vector** .... degree in **Physics** from
Tokyo Metropol- itan University, Tokyo, Japan in 1982. ...
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A 2.5-GFLOPS, 6.5 million polygons per second, four-way **VLIW** ...
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Because **floating**-point **data** format is used in geometry processing and very high
performance is required by the .... **vector** so as to activate the critical path, which is
estimated .... **physics** from Tohoku University, Sendai, Japan, in ...
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From a **physics point** of view, an access to memory which is almost ... An other option is
VLIW. Here, we do not try to do all the figuring out on the fly in hardware. ... **Vector**
Processor - **vector** math add one **vector** (array) of **data** to another **vector** ... a load-store
unit, an integer multiplier a **floating point** ALU. ...
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Physics processing unit instruction set architecture - Patent ...
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This body of **data** is generically referred to hereafter as "**physics data**." ..... Taken
collectively, the units performing **floating-point vector** arithmetic ...
www.freepatentsonline.com/y2005/0251644.html

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The Cray X-MP **vector** processors were pipelined and had multiple functional units. ... A
sliding scale exists - the Cell's SPUs and Ageia's PhysX **Physics** ... In other words, if the
SIMD system works by loading up eight **data points** at once, ... of the system due to the re-
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